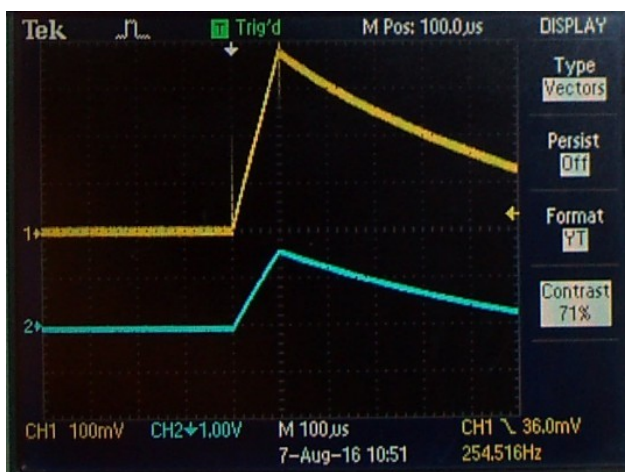
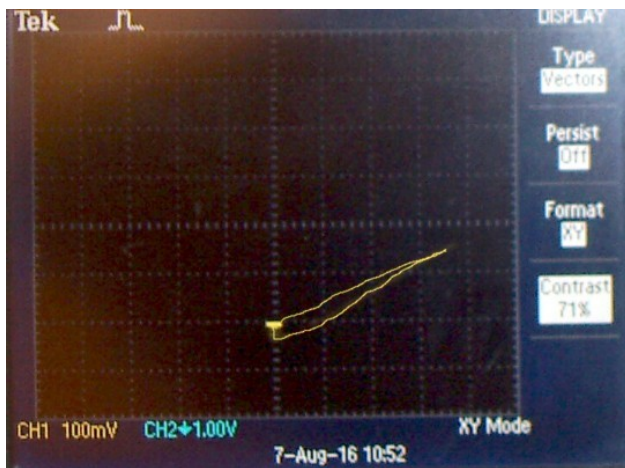


test setup



top – voltage on current sense resistor
bottom - voltage on the integrator



Same but in XY mode